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8-bit PSW 8-bit SP Internal ROM and EPROM (8751) Internal RAM of 256 bytes Architecture of 8051: Registers in 8051 Math Register * indicates that each bit a of the register can be programmed.

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â€¢ Also called embedded controllers, because the microcontroller and support circuits are often built into, or embedded in, the devices they control. â€¢ Devices that utilize microcontrollers include car engines, consumer electronics

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The Intel MCS-51 (commonly termed 8051) is an internally Harvard architecture, complex instruction set computer (CISC) instruction set, single chip microcontroller ($\hat{A}\mu\text{C}$) series developed by Intel in 1980 for use in embedded systems.

Intel MCS-51 - Wikipedia

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